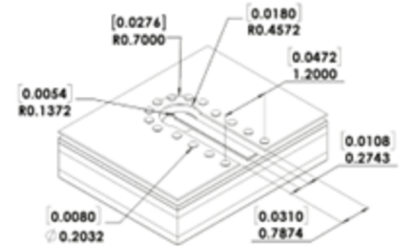
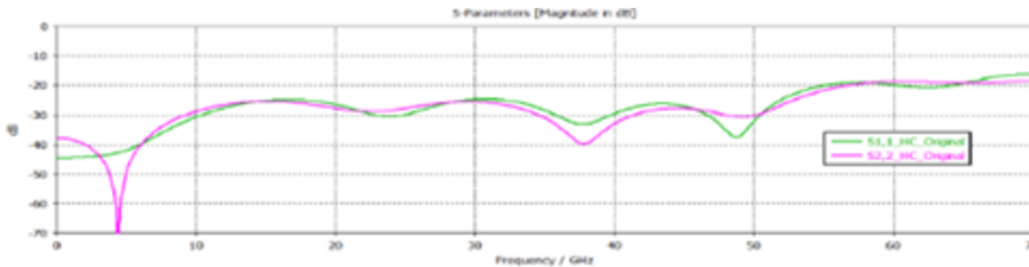


PCB Launch Optimization Services

INTRODUCTION



The Amphenol CDI team ensures seamless integration of CoreHC™ and other solderless precision connectors into your platform. Our simulation services optimize your footprint based on your PCB stack-up, supported by encrypted HFSS models for independent analysis.

Recognizing the PCB's critical role in connector performance, we factor in signal transition dimensions, layer count, dielectric materials, and routing styles to deliver tailored solutions that meet target specifications and frequency requirements.

After optimization, we collaborate with your team to finalize the footprint layout. Since the CoreHC™ connector may be unfamiliar to some layout engineers, we provide expert guidance throughout the design process to ensure smooth implementation.

KEY BENEFITS

- Custom-tuned footprint to any specific PCB stack-up
- Full S-Parameter and s2p output
- Xtalk-specific optimizations available
- Reduced layout workload for engineers

